



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Steven P. Young
Assignee: Xilinx, Inc.
Title: "A Programmable Multi-Chip Module"
Serial No.: 10/624,832 File Date: 07-21-03
Examiner: Anh Q. Tran Art Unit: 2819
Docket No.: X-1335 US Conf.No.: 6570

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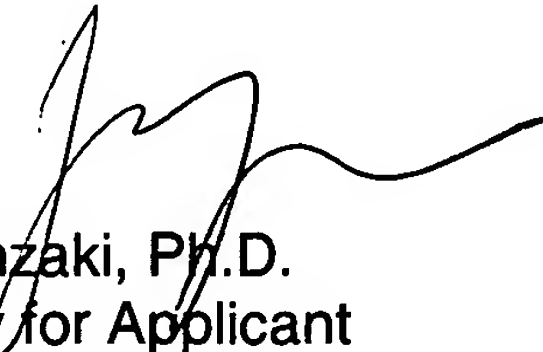
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the two (2) references listed in the attached Substitute for Form 1449A/PTO.

The enclosed references are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,


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Reg. No. 37,652

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PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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Examiner Signature		Date Considered	
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